## FACULTY PROFILE

| | | |
|---|---|---|
| 1. | Name | SPOORTHI S P |
| 2. | Date of Birth (dd/mm/yyyy) | 28/05/1989 |
| 3. | UID (Aadhar No.) | ########5147 |
| 4. | e-Mail ID (Official) | Spoorthi.sp@atria.edu |
| 5. | Designation | Assistant Professor |
| 6. | Department | Electronics and Communication |
| 7. | Google Scholar Link: | https://scholar.google.com/citations?user=RXpKRRAAAAAJ&hl=en |
| 8. | Area of Specialization | VLSI Design and Embedded Systems |
| 9. | Courses Taught (UG) | VLSI Design, Digital switching systems, information theory coding, digital system design, DSP algorithms and architecture, Power Electronics, Verilog HDL |
| 10. | Courses Taught (PG) | Nil |
| 11. | QUALIFICATION DETAILS: | B.tech( Electronics and Communication) M.tech(VLSI design and embedded systems) |

| | | | | |
|---|---|---|---|---|
| 12. | Total Experience in Years | Teaching | Industry | Research |
| | | 3.5 | 0 | 0 |

| | | | National | | International |
|---|---|---|---|---|---|
| 13. | Publication Details | Type/Level | National | | International |
| | | Journal | | | 2 |
| | | Conference | 1 | | 1 |

| | | |
|---|---|---|
| 14. | No. of Research and Projects Guided | 04 |
| 15. | No. of Books Published/IPRs/Patents | - |
| 16. | Sponsored Projects/Grants (if any) | - |
| 17. | Professional Memberships | - |
| 18. | Coordinator/In-Charge/Part, of NAAC/NBA/any other Accreditations. | Time table coordinator Class coordinator Part of NBA ERP Coordinator Part of NAAC |
| 19. | Academic Achievements | - |
| 20. | Extra-Curricular Achievements | - |
| 21. | Co-Curricular Achievements | Completed course in Coursera how to make a MOOC? IOT |
| 22. | No. of Workshops/Seminars Conducted | Nil |
| 23. | No. of Workshops/Seminars Attended | 10 webinar and 4 FDP |
| 24. | Sports Achievements | Won 2nd place in "ball badminton" District level competition |
| 25. | Awards and Recognition (If any) | Nil |